

IRHNJ7130 (JANSR2N7380U3)

PD-93820E

Radiation Hardened Power MOSFET

Surface Mount (SMD-0.5)

100V, 14.4A, N-channel, Rad Hard HEXFET™ Technology

Features

- Single event effect (SEE) hardened
- Low $R_{DS(on)}$
- Low total gate charge
- Simple drive requirements
- Hermetically sealed
- Light weight
- Surface Mount
- ESD rating: Class 1C per MIL-STD-750, Method 1020

Potential Applications

- DC-DC converter
- Motor drives
- Thermal management

Product Validation

Qualified to JANS screening flow according to MIL-PRF-19500 for space applications

Description

IR HiRel rad hard HEXFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low $R_{DS(on)}$ and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well-established advantages of MOSFETs such as voltage control, fast switching and temperature stability of electrical parameters.

Ordering Information

Table 1 **Ordering options**

Part number	Package	Screening Level	TID Level
IRHNJ7130	SMD-0.5	COTS	100 krad(Si)
JANSR2N7380U3	SMD-0.5	JANS	100 krad(Si)
IRHNJ3130	SMD-0.5	COTS	300 krad(Si)
JANSF2N7380U3	SMD-0.5	JANS	300 krad(Si)
IRHNJ4130	SMD-0.5	COTS	500 krad(Si)
JANSF2N7380U3	SMD-0.5	JANS	500 krad(Si)

Product Summary

- **BV_{DSS}** : 100V
- **I_D** : 14.4A
- **$R_{DS(on),max}$** : 180m Ω
- **$Q_{G,max}$** : 40nC
- **REF**: MIL-PRF-19500/614

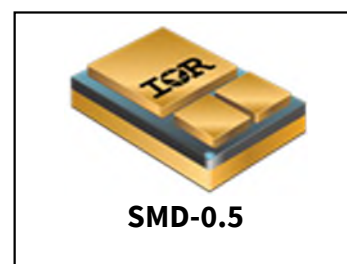


Table of contents

Table of contents

Features	1
Potential Applications.....	1
Product Validation.....	1
Description	1
Ordering Information.....	1
Table of contents.....	2
1 Absolute Maximum Ratings	3
2 Device Characteristics	4
2.1 Electrical Characteristics (Pre-Irradiation).....	4
2.2 Source-Drain Diode Ratings and Characteristics (Pre-Irradiation)	5
2.3 Thermal Characteristics	5
2.4 Radiation Characteristics.....	5
2.4.1 Electrical Characteristics — Post Total Dose Irradiation	5
2.4.2 Single Event Effects — Safe Operating Area	6
3 Electrical Characteristics Curves (Pre-irradiation)	7
4 Test Circuits (Pre-irradiation)	10
5 Package Outline.....	11
Revision history.....	12

Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings (Pre-Irradiation)

Symbol	Parameter	Value	Unit
$I_{D1} @ V_{GS} = 12V, T_C = 25^{\circ}C$	Continuous Drain Current	14.4	A
$I_{D2} @ V_{GS} = 12V, T_C = 100^{\circ}C$	Continuous Drain Current	9.1	A
$I_{DM} @ T_C = 25^{\circ}C$	Pulsed Drain Current ¹	57.6	A
$P_D @ T_C = 25^{\circ}C$	Maximum Power Dissipation	75	W
	Linear Derating Factor	0.6	W/ $^{\circ}C$
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy ²	150	mJ
I_{AR}	Avalanche Current ¹	14.4	A
E_{AR}	Repetitive Avalanche Energy ¹	7.5	mJ
dv/dt	Peak Diode Reverse Recovery ³	6.0	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	$^{\circ}C$
	Lead Temperature	300 (for 5 sec)	
	Weight	1.0 (Typical)	

¹ Repetitive Rating; Pulse width limited by maximum junction temperature.² $V_{DD} = 25V$, starting $T_J = 25^{\circ}C$, $L = 1.4mH$, Peak $I_L = 14.4A$, $V_{GS} = 12V$ ³ $I_{SD} \leq 14.4A$, $di/dt \leq 395A/\mu s$, $V_{DD} \leq 100V$, $T_J \leq 150^{\circ}C$

Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics (Pre-Irradiation)

Table 3 Static and Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (Unless Otherwise Specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	—	V	$V_{GS} = 0V, I_D = 1.0mA$
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.11	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1.0mA$
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	180	$m\Omega$	$V_{GS} = 12V, I_{D2} = 9.1A^1$
		—	—	200		$V_{GS} = 12V, I_{D2} = 14.4A^1$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} = V_{GS}, I_D = 1mA$
G_{fs}	Forward Transconductance	2.5	—	—	S	$V_{DS} = 15V, I_{D2} = 9.1A^1$
I_{DSS}	Zero Gate Voltage Drain Current	—	—	25	μA	$V_{DS} = 80V, V_{GS} = 0V$
		—	—	250		$V_{DS} = 80V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Leakage Reverse	—	—	-100		$V_{GS} = -20V$
Q_G	Total Gate Charge	—	—	40	nC	$I_{D1} = 14.4A$
Q_{GS}	Gate-to-Source Charge	—	—	10		$V_{DS} = 50V$
Q_{GD}	Gate-to-Drain ('Miller') Charge	—	—	20		$V_{GS} = 12V$
$t_{d(on)}$	Turn-On Delay Time	—	—	35	ns	$I_{D1} = 14.4A^{**}$ $V_{DD} = 50V$ $R_G = 7.5\Omega$ $V_{GS} = 12V$
t_r	Rise Time	—	—	75		
$t_{d(off)}$	Turn-Off Delay Time	—	—	40		
t_f	Fall Time	—	—	30		
$L_s + L_D$	Total Inductance	—	4.0	—	nH	Measured from center of Drain pad to center of Source pad
C_{iss}	Input Capacitance	—	960	—	pF	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1.0MHz$
C_{oss}	Output Capacitance	—	340	—		
C_{rss}	Reverse Transfer Capacitance	—	85	—		

** Switching speed maximum limits are based on manufacturing test equipment and capability.

¹ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$

Device Characteristics

2.2 Source-Drain Diode Ratings and Characteristics (Pre-Irradiation)

Table 4 Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
I _S	Continuous Source Current (Body Diode)	—	—	14.4	A	
I _{SM}	Pulsed Source Current (Body Diode) ¹	—	—	57.6	A	
V _{SD}	Diode Forward Voltage	—	—	1.5	V	T _J = 25°C, I _S = 14.4A, V _{GS} = 0V ²
t _{rr}	Reverse Recovery Time	—	—	275	ns	T _J = 25°C, I _F = 14.4A, V _{DD} ≤ 25V di/dt = 100A/μs ²
Q _{rr}	Reverse Recovery Charge	—	0.8	—	μC	
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)				

2.3 Thermal Characteristics

Table 5 Thermal Resistance

Symbol	Parameter	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction-to-Case	—	—	1.67	$^\circ\text{C}/\text{W}$

2.4 Radiation Characteristics

IR HiRel radiation hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at IR HiRel is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 3 and 4) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

2.4.1 Electrical Characteristics — Post Total Dose Irradiation

Table 6 Electrical Characteristics @ $T_J = 25^\circ\text{C}$, Post Total Dose Irradiation ^{3, 4}

Symbol	Parameter	100 krad (Si) ⁵		Up to 500 krad (Si) ⁶		Unit	Test Conditions
		Min.	Max.	Min.	Max.		
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	100	—	V	$V_{GS} = 0\text{V}$, $I_D = 1.0\text{mA}$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0	1.25	4.5	V	$V_{DS} = V_{GS}$, $I_D = 1.0\text{mA}$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	—	100	nA	$V_{GS} = 20\text{V}$
	Gate-to-Source Leakage Reverse	—	-100	—	-100		$V_{GS} = -20\text{V}$
I_{DSS}	Zero Gate Voltage Drain Current	—	25	—	25	μA	$V_{DS} = 80\text{V}$, $V_{GS} = 0\text{V}$
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance (TO-3) ²	—	180	—	240	$\text{m}\Omega$	$V_{GS} = 12\text{V}$, $I_{D2} = 9.1\text{A}$
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance (SMD-0.5) ²	—	180	—	240	$\text{m}\Omega$	$V_{GS} = 12\text{V}$, $I_{D2} = 9.1\text{A}$
V_{SD}	Diode Forward Voltage	—	1.5	—	1.5	V	$V_{GS} = 0\text{V}$, $I_F = 14.4\text{A}$

¹ Repetitive Rating; Pulse width limited by maximum junction temperature.

² Pulse width $\leq 300 \mu\text{s}$; Duty Cycle $\leq 2\%$

³ Total Dose Irradiation with V_{GS} Bias. $V_{GS} = 12\text{V}$ applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, Method 1019, condition A.

⁴ Total Dose Irradiation with V_{DS} Bias. $V_{DS} = 80\text{V}$ applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, Method 1019, condition A.

⁵ Part numbers IRHNJ7130 (JANSR2N7380U3)

⁶ Part numbers IRHNJ3130 (JANSF2N7380U3) and IRHNJ4130 (JANS2N7380U3)

IRHNJ7130 (JANSR2N7380U3)
Radiation Hardened Power MOSFET Surface Mount (SMD-0.5)

Device Characteristics

2.4.2 Single Event Effects — Safe Operating Area

IR HiRel radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. 1 and Table 7.

Table 7 Typical Single Event Effects Safe Operating Area

LET (MeV·cm ² /mg)	Energy (MeV)	Range (μm)	V _{DS} (V)				
			V _{GS} = 0V	V _{GS} = -5V	V _{GS} = -10V	V _{GS} = -15V	V _{GS} = -20V
28 ± 5%	283.3 ± 7.5%	42.8 ± 5%	100	100	100	80	60
37 ± 5%	305 ± 5%	39 ± 5%	100	90	70	50	—

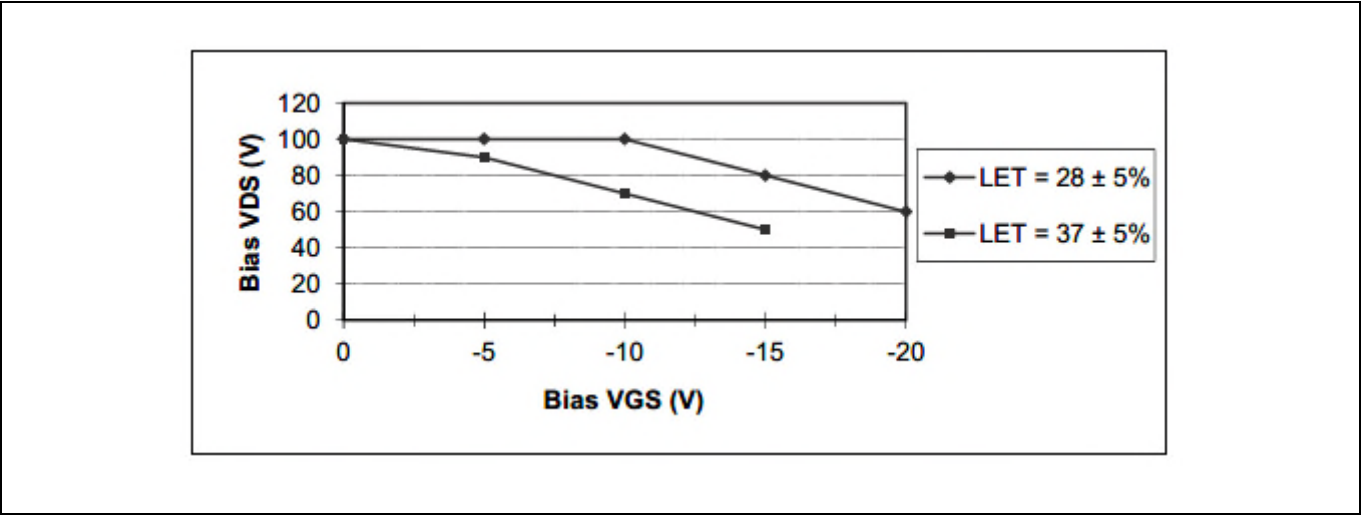


Figure 1 Typical Single Event Effect, Safe Operating Area

IRHNJ7130 (JANSR2N7380U3)

Radiation Hardened Power MOSFET Surface Mount (SMD-0.5)

Electrical Characteristics Curves (Pre-irradiation)

3 Electrical Characteristics Curves (Pre-irradiation)

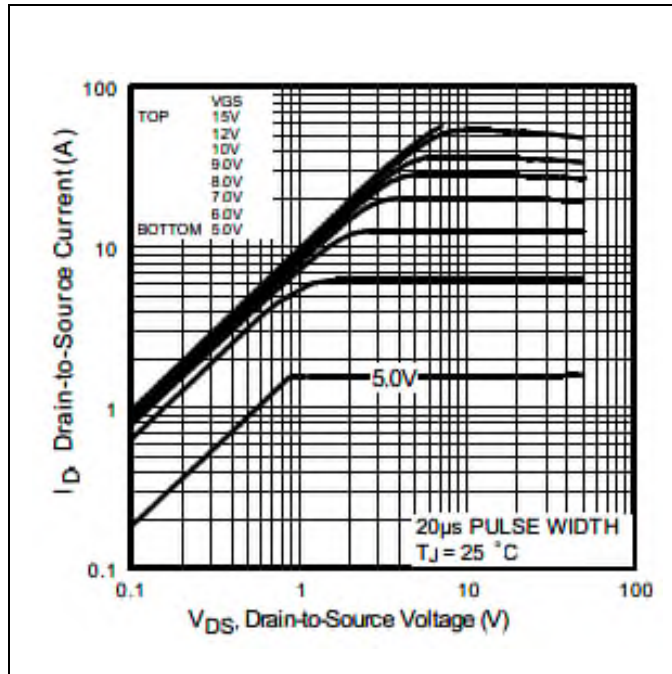


Figure 2 Typical Output Characteristics

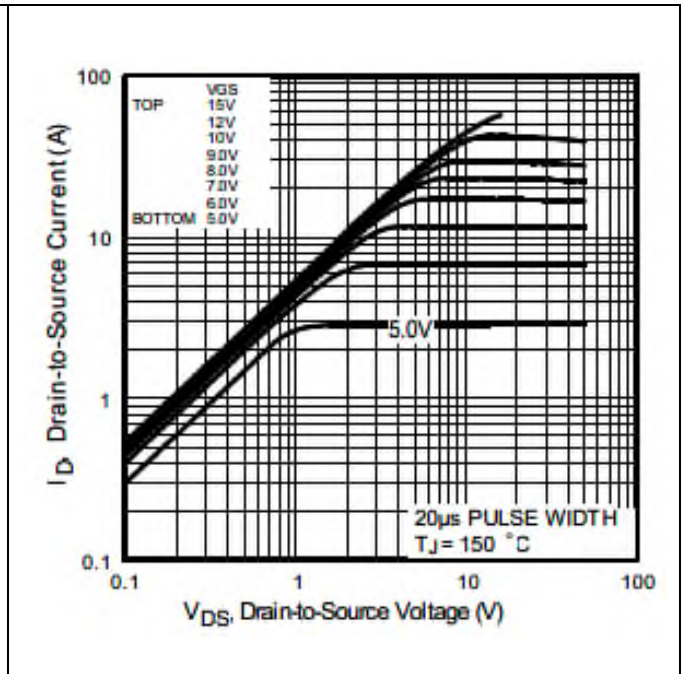


Figure 3 Typical Output Characteristics

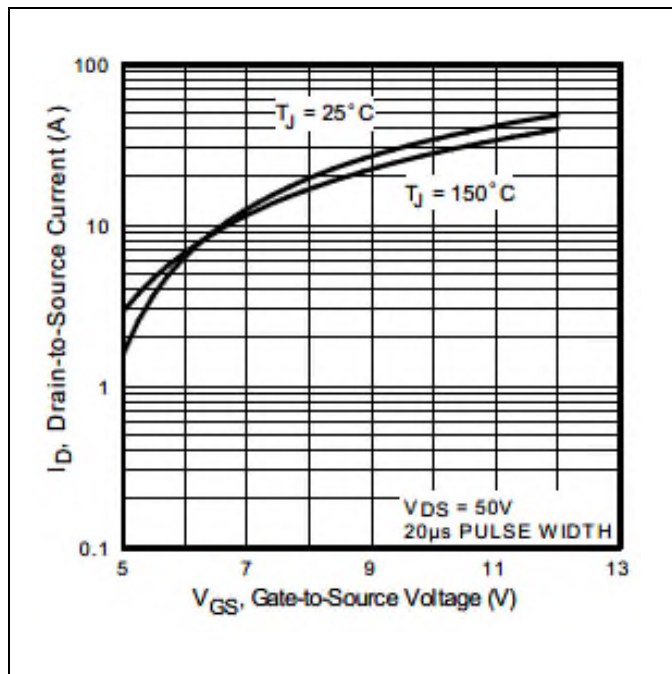


Figure 4 Typical Transfer Characteristics

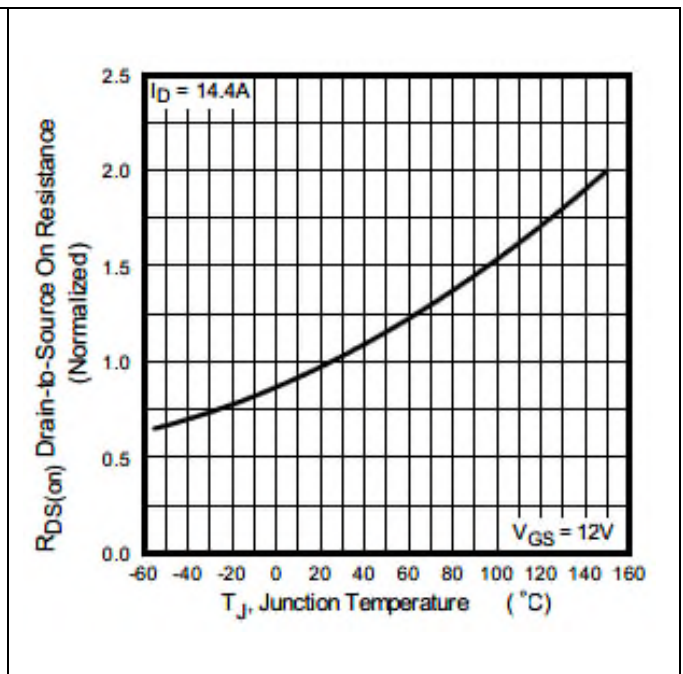


Figure 5 Normalized On-Resistance Vs. Temperature

IRHNJ7130 (JANSR2N7380U3)

Radiation Hardened Power MOSFET Surface Mount (SMD-0.5)

Electrical Characteristics Curves (Pre-irradiation)

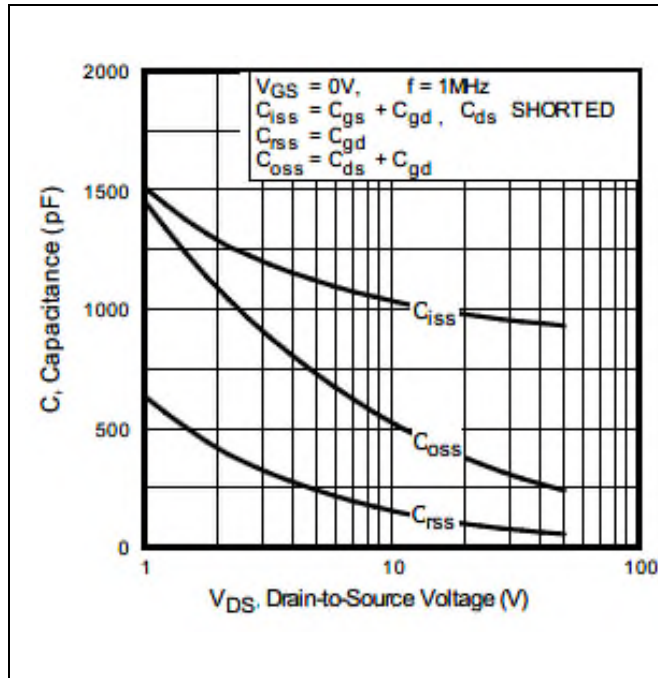


Figure 6 Typical Capacitance Vs. Drain-to-Source Voltage

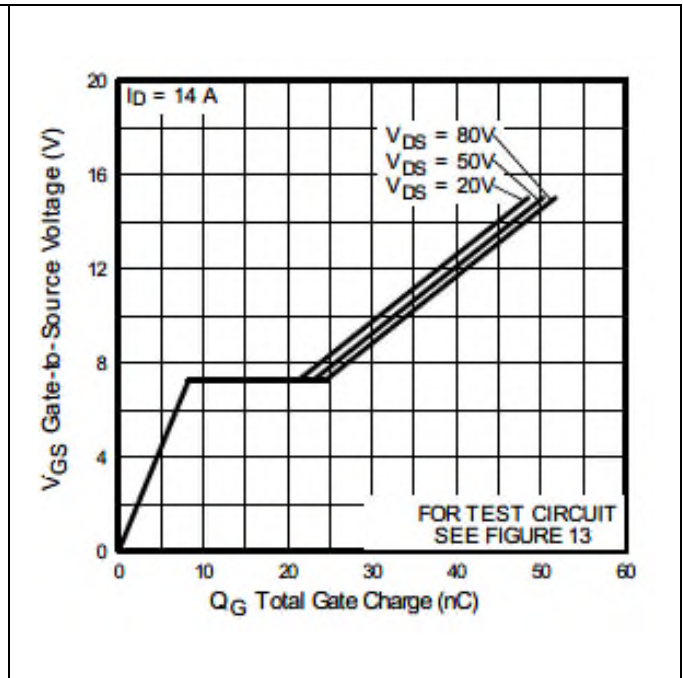


Figure 7 Typical Gate-to-Source Voltage Vs. Typical Gate Charge

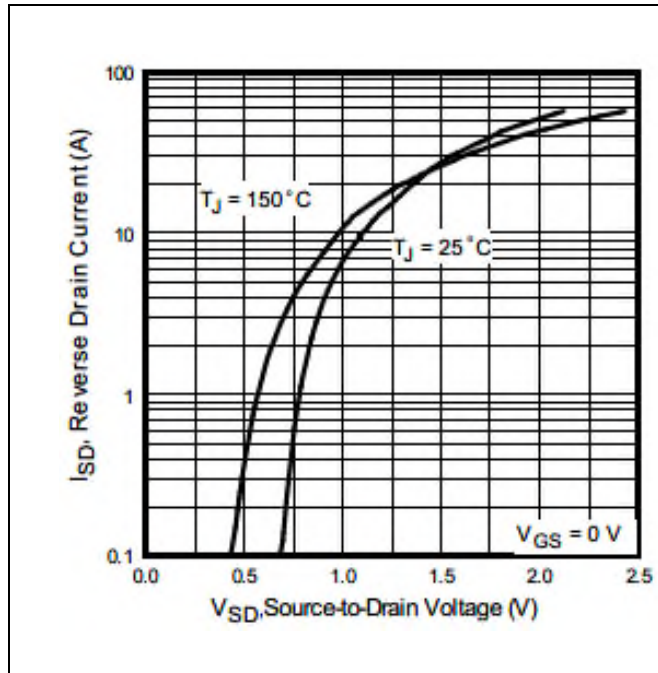


Figure 8 Typical Source-Drain Current Vs. Diode Forward Voltage

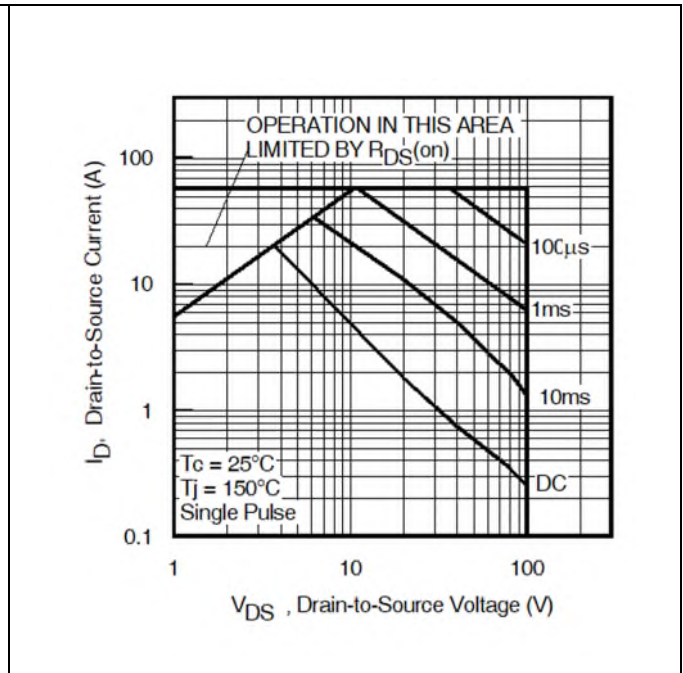


Figure 9 Maximum Safe Operating Area

IRHNJ7130 (JANSR2N7380U3)

Radiation Hardened Power MOSFET Surface Mount (SMD-0.5)

Electrical Characteristics Curves (Pre-irradiation)

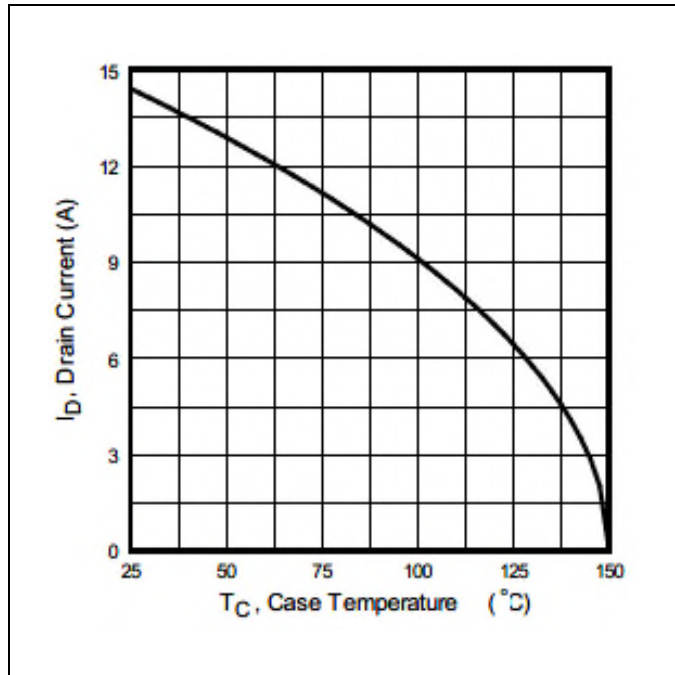


Figure 10 Maximum Drain Current Vs. Case Temperature

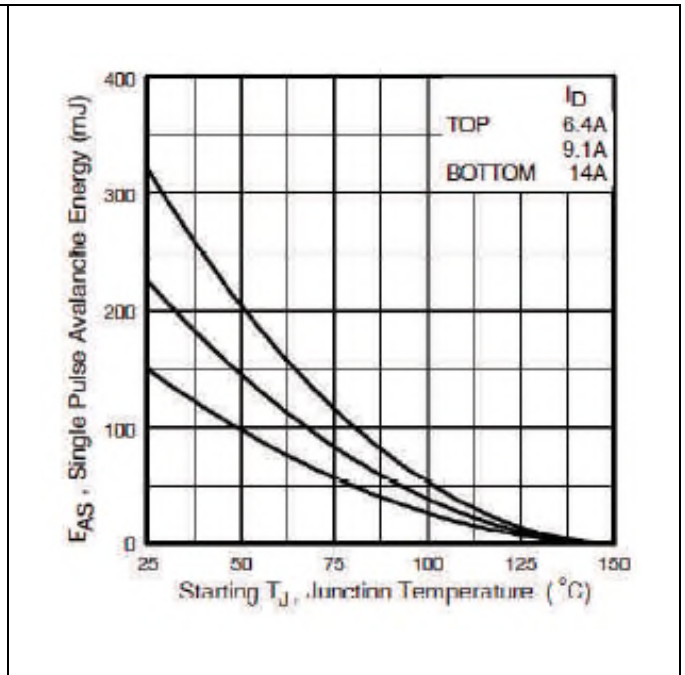


Figure 11 Maximum Avalanche Energy Vs. Junction Temperature

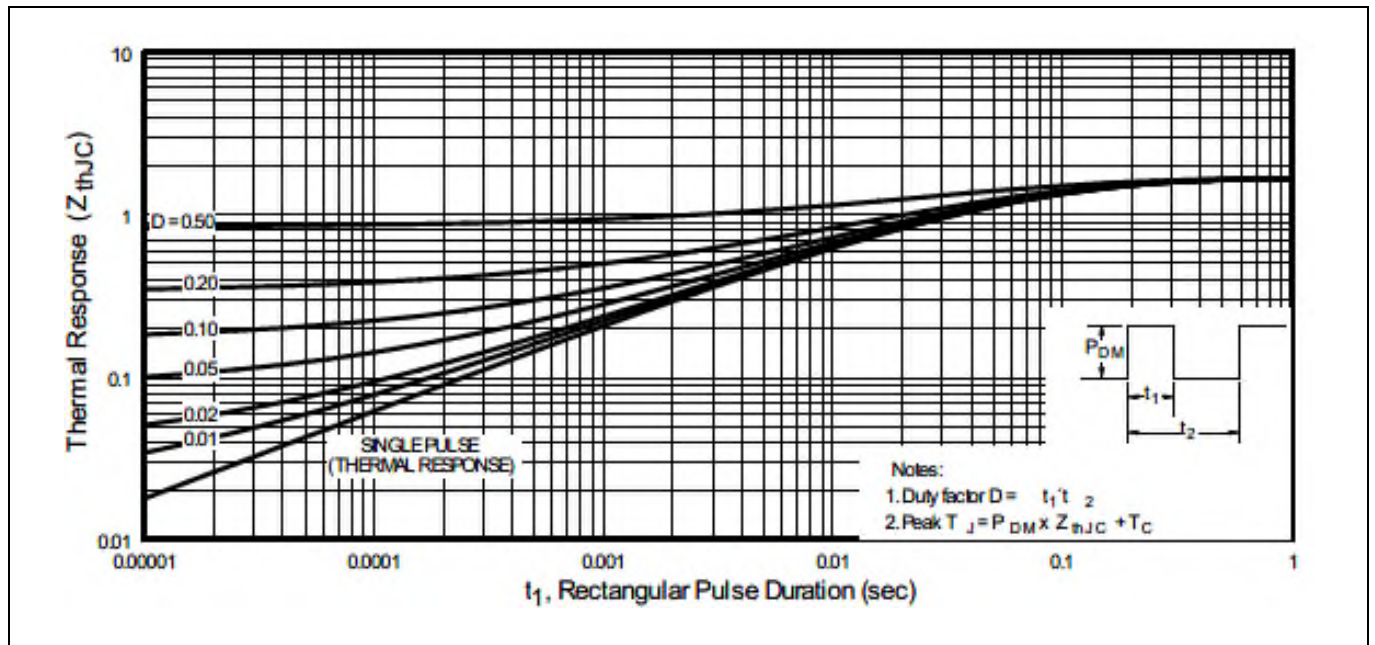
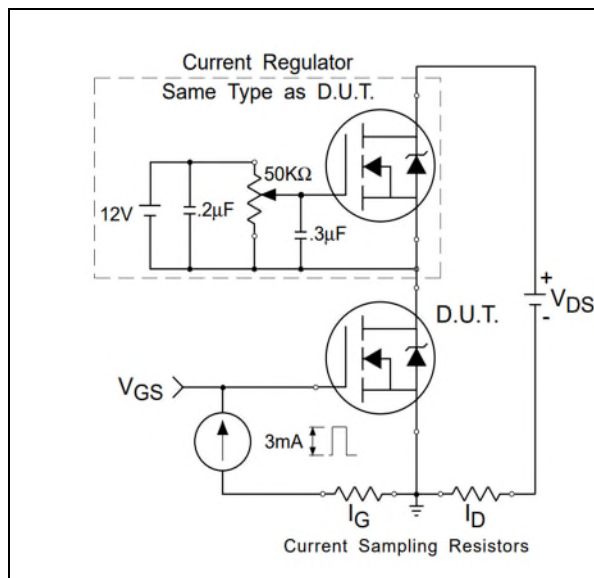


Figure 12 Maximum Effective Transient Thermal Impedance, Junction-to-Case



The graph shows the gate voltage V_G as a function of charge. The voltage starts at 0, rises linearly to a plateau at 12 V, remains constant, and then rises linearly again to a final plateau. The first plateau is labeled Q_{GS} and the second is labeled Q_{GD} . The total charge for the first stage is labeled Q_G .

[illegible]

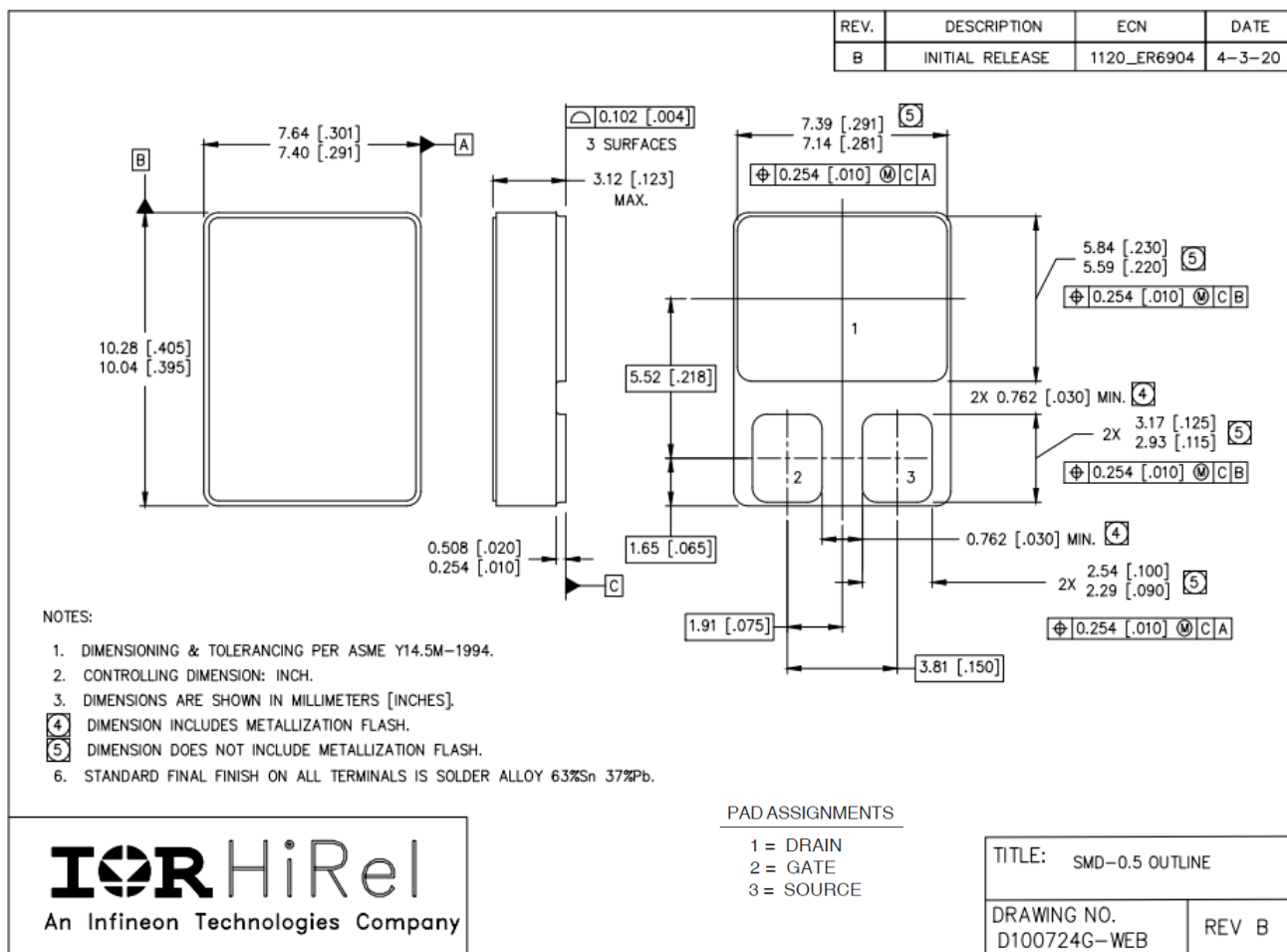
The diagram shows two waveforms: V_{DS} (drain-source voltage) and V_{GS} (gate-source voltage). V_{GS} is a square wave that transitions between a high level and a low level. V_{DS} is a trapezoidal wave that transitions between a high level and a low level in response to V_{GS} . The transitions are labeled with time intervals: $t_{d(on)}$ (delay time to turn on), t_r (rise time), $t_{d(off)}$ (delay time to turn off), and t_f (fall time). The high level of V_{DS} is marked as 90% and the low level as 10%.

2022-10-14

Package Outline

5 Package Outline

Note: For the most updated package outline, please see the website: [SMD-0.5](#)



Revision history**Revision history**

Document version	Date of release	Description of changes
	03/04/2000	Datasheet (PD-93820)
Rev A	05/20/2006	Updated 600KRad(si) to 500KRad(si)
Rev B	10/10/2017	Updated based on ECN-1120_05749
Rev C	02/05/2019	Updated based on ECN-1120_06897
Rev D	05/27/2022	Updated based on ECN-1120_09018
Rev E	10/14/2022	Updated based on ECN-1120_08924

Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

Edition 2022-10-14

Published by

**International Rectifier HiRel Products,
Inc.**

**An Infineon Technologies company
El Segundo, California 90245 USA**

**© 2022 Infineon Technologies AG.
All Rights Reserved.**

**Do you have a question about this
document?**

Email: erratum@infineon.com

Document reference

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenhheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

WARNINGS

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest International Rectifier HiRel Products, Inc., an Infineon Technologies company, office.

International Rectifier HiRel Components may only be used in life-support devices or systems with the expressed written approval of International Rectifier HiRel Products, Inc., an Infineon Technologies company, if failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety and effectiveness of that device or system.

Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.