

HFA40HF120C

PD-91797D

Ultrafast, Soft Recovery Diode Surface Mount (SMD-1) 1200V, 15A

Features

- Reduced RFI and EMI
- Reduced snubbing
- Extensive characterization of recovery parameters
- · Hermetic package
- Surface mount

Product Summary

V_R: 1200V

V_F: 4.4V

• **t**_{rr}: 100ns

• **Q**_{rr}: 370nC

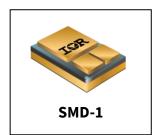
• **di**_{(rec)M}/**dt:** 380A/μs

Potential Applications

- DC-DC converter
- Motor drives

Product Validation

Qualified according to MIL-PRF-19500 for space applications



Description

HFA40HF120C is part of the IR HiRel family of products. These Ultrafast, soft recovery diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and di/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motor drives and other applications where switching losses are significant portion of the total losses.

Ordering Information

Table 1 Ordering options

Part number	Package	Screening Level		
HFA40HF120C	SMD-1	COTS		
HFA40HF120SCV	SMD-1	JANTXV-equivalent		

HFA40HF120C

FRED Ultrafast, Soft Recovery Diode



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Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_R	Cathode to anode voltage	1200	V
I _{F(AV)}	Continuous forward current, T _C = 100°C ¹	15	Α
I _{FSM}	Single pulse forward current, T _C = 25°C ²	50	Α
P _D @ T _C = 25°C	Maximum power dissipation	63	W
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150	°C
Wt	Weight	2.6 (Typical)	g

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¹ DC = 50% rectangle wave

 $^{^2}$ ½ sine wave, 60 Hz, Pulse width = 8.33 ms



Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics

Table 3 Electrical Characteristics (Per Leg) @ T_J =25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
V_{BR}	Cathode Anode Breakdown Voltage	1200	_	_	V	I _R = 250μA
V_{F}		_	_	3.9	V	$I_F = 7.0A, T_J = -55^{\circ}C$
	Max Forward Voltage Drop See Fig. 1	_	_	3.3	V	$I_F = 7.0A, T_J = 25^{\circ}C$
		_	_	4.4	V	I _F = 15A, T _J = 25°C
		_	_	2.8	V	$I_F = 7.0A, T_J = 125^{\circ}C$
	Max Reverse Leakage Current	_	_	10	μΑ	$V_R = V_R$ Rated
I _R	See Fig. 2	_	_	1.0	mA	V _R = 960V, T _J = 125°C
CJ	Junction Capacitance See Fig. 3	_	15	20	pF	V _R = 200V
L _s	Series Inductance	_	2.8	_	nH	Measured from center of cathode pad to center of anode pad

2.2 Dynamic Recovery Characteristics

Table 4 Dynamic Recovery Characteristics (Per Leg) @ T_J =25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
t _{rr1}	Reverse Recovery Time	_	58	100		T _J = 25°C	
t _{rr2}	See Fig. 5	_	110	165	ns	T _J = 125°C	$I_F = 7.0A$
I _{RRM1}	Peak Recovery Current	_	5.4	8.1		T _J = 25°C	
I _{RRM2}	See Fig. 6	_	7.2	10.8	- A	T _J = 125°C	V _R = 200V
Q _{rr1}	Reverse Recovery Charge	_	185	370		T _J = 25°C	
Q _{rr2}	See Fig. 7	_	395	590	nC	T _J = 125°C	$d_{if}/dt = 200 A/ \mu s$
$di_{(rec)M}/dt_1$	Peak Rate of Fall of Recovery	_	255	380		T _J = 25°C	
$di_{(rec)M}/dt_2$	Current During t₀ See Fig. 8	_	160	240	A/ μs	T _J = 125°C	

2.3 Thermal-Mechanical Characteristics

 Table 5
 Thermal-Mechanical Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}$	Junction to Case, Single Leg Conducting		2.0	°C/W



Electrical Characteristics Curves

3 Electrical Characteristics Curves

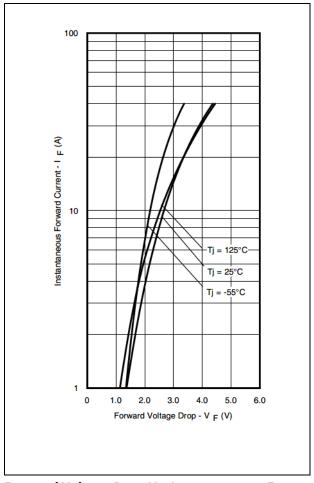


Figure 1 Typical Forward Voltage Drop Vs. Instantaneous Forward Current (Per Leg)

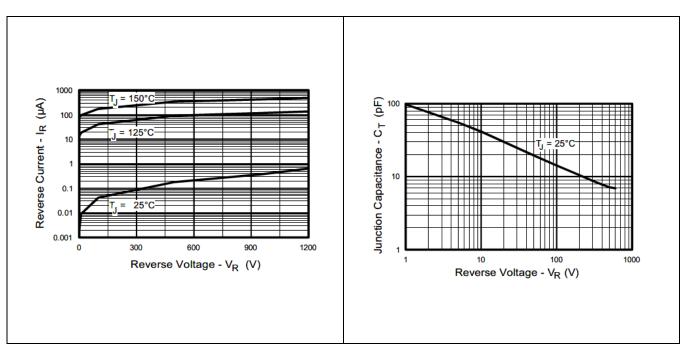


Figure 2 Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

Figure 3

Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

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Electrical Characteristics Curves

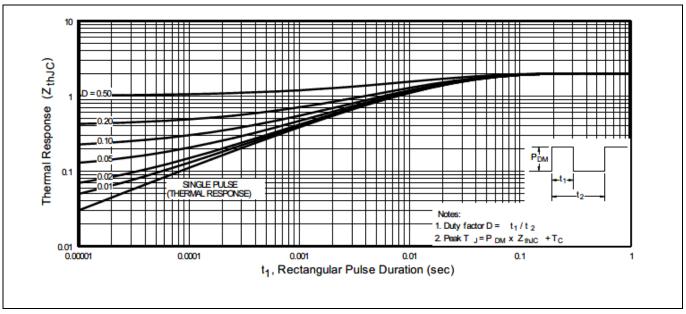


Figure 4 Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

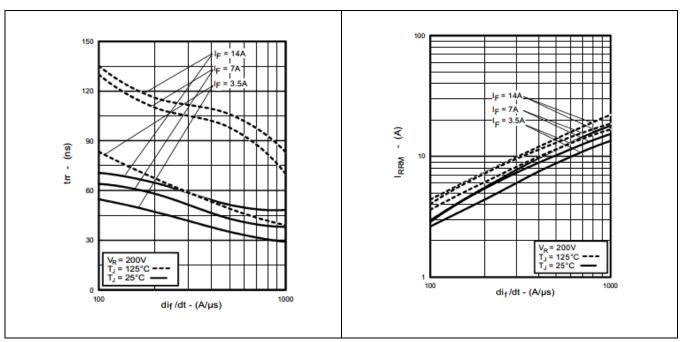


Figure 5 Typical Reverse Recovery Vs. di_f/dt (Per Leg)

Figure 6 Typical Recovery Current Vs. di_f/dt (Per Leg)

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Electrical Characteristics Curves

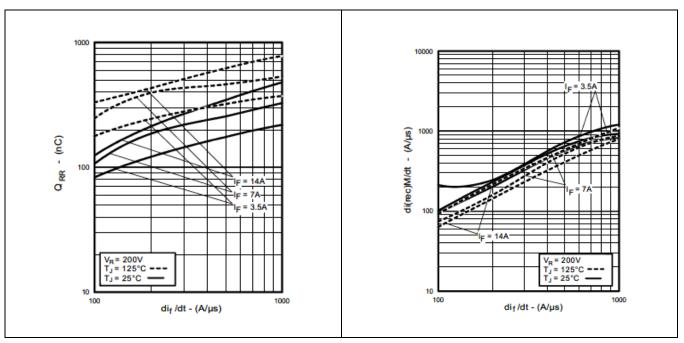


Figure 7 Typical Stored Charge Vs. di_f/dt (Per Leg)

 $Figure \, 8 \qquad Typical \, di_{(rec)M}/dt \, Vs. \, di_f/dt \, (Per \, Leg)$



Test Circuit

4 Test Circuit

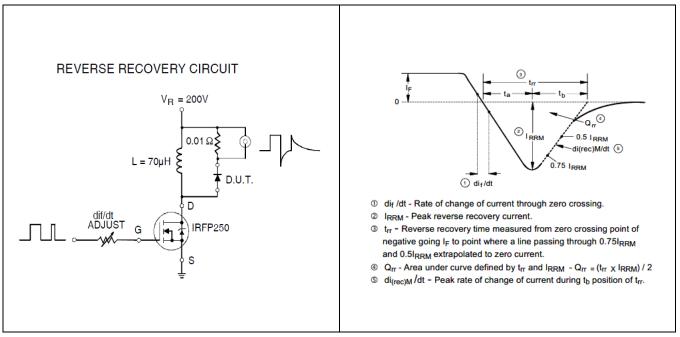


Figure 9 Reverse Recovery Parameter Test Circuit

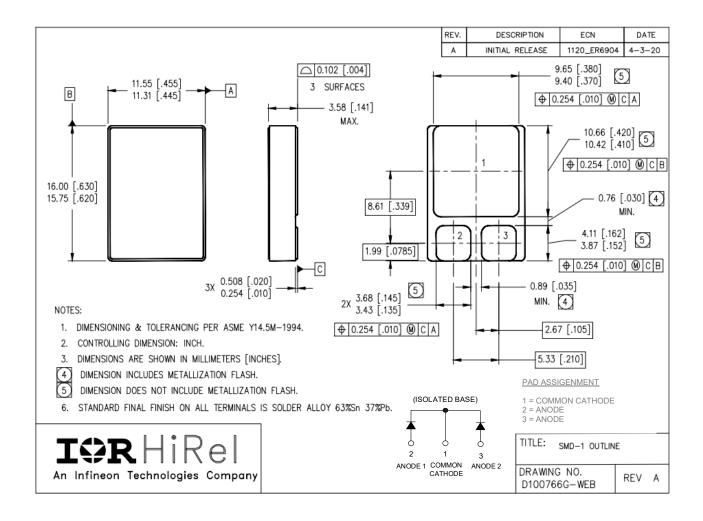
Figure 10 Reverse Recovery Waveform and Definitions



Package Outline

5 Package Outline

Note: For the most updated package outline, please see the website: **SMD-1**



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Revision history

Revision history

Document version	Date of release	Description of changes
	04/30/1998	Final datasheet (PD-91797)
Rev A	12/14/2015	Updated per ECN-1120-03627
Rev B	09/21/2016	Updated per ECN-1120-04688
Rev C	10/13/2017	Updated per ECN-1120-05515
Rev D	05/31/2024	Updated per ECN-1120-09961

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