

XDPE1A2G7B Digital Multi-phase Controller

16-phase Dual Loop Voltage Regulator

Features

- Digital controller assisted with high performance analog front ends with a fully programmable ARM® Cortex™ –M0 Processor
- Compliant with nVIDIA custom PWMVID specification
- Compliant with PMBus rev 1.3.1 (March 2015) serial interface
 - Query voltage, current, temperature faults
 - Fault Response
 - Maximum supported bus speed 1 MHz
 - Support 1.8/3.3 V operation
- Output voltage regulation range
 - 0.05 V to 2.75 V (5 mV/step)
- Digitally controlled phasing for full flexibility in both loop configuration and phase firing order
- Configurable autonomous phase add/drop
- Automatic phase detection at start-up
- Supports start-up into pre-bias voltage
- Integrated power stage current sense
- Differential output voltage sense
- Fast current balancing with current-mode control
- Digitally programmable PID (Proportional, Integral, Differential) loop compensation
- Digitally programmable load line slope and offset
- Digital temperature compensation
- Direct input (+12 V) voltage and current sense
- Extensive fault detection and protection capability
 - IUVP, OUVF & OOVF (fixed and tracking)
 - OCP instantaneous & averaged (total current)
 - Multiple OTP thresholds
 - Peak phase current pulse limiting
 - Negative current limit protection
- Internal non-volatile memory (NVM) to store multiple custom configurations
- RoHS compliant and Halogen free 56-lead QFN plastic package

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Potential applications

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- Vcore power regulation for AI based Microprocessors
 - Servers
 - Workstations
 - High-end desktop GPUs

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22

Description

The XDPE1A2G7B digital dual loop 16 phase controller provides power for nVIDIA AI servers, workstation, and high-end desktop GPU applications. Voltage on either the first loop or the second loop can be provided by a multi-phase buck converter with up to 16 synchronous-rectified channels on the first loop and up to 8 phases on the second loop. Typical configurations of 16+0, 15+1, and up to 8+8 are supported.

Command and monitoring functions are controlled through the PMBus and PWMVID interfaces which supports dynamic voltage identification with 5 mV/step, output range up to 2.75 V, offset and trim resolution of 625 μ V and accuracy better than 0.5%.

The XDPE1A2G7B controller utilizes digital technology to implement all control functions, providing the ultimate system solution in terms of flexibility and stability. Advanced control loop features, such as current mode control, variable frequency operation, Active Transient Response (ATR/FATR), fast DVID response, automatic phase shedding and Pulse Frequency Modulation (PFM) enable optimal response to a highly dynamic load with fast di/dt load transients across a wide range of load current.

The XDPE1A2G7B controller supports multi-vendor pin compatible integrated power stages with trivalent PWM interfaces, fast response, integrated current sense, integrated temperature sense, and advanced fault detection capability, interface and signaling.

In addition to supporting power stage fault protection features, the XDPE1A2G7B controller also includes a set of sophisticated over-voltage, under-voltage, over-temperature, over-current, and phase fault protections. These attributes provide a complete and advanced protection feature set for microprocessor and power systems.

Infineon strongly recommends pairing Infineon's power stages with our Digital XDP™ family of controllers to ensure correct interoperability. Interoperability when pairing with other vendor power stages/ discrete power components cannot be guaranteed by Infineon and requires thorough evaluation and characterization by the power stage/ discrete power component vendor.

Table 1 Part number and package summary

Part Number	Package
XDPE1A2G7B	56-lead 7 mm x 7 mm QFN PG-VQFN-56

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1 Ordering information

Table 2 Ordering information

Base Part Number	Package Type	Standard Pack Form and Quantity		Orderable Part Number
XDPE1A2G7B	QFN 7 mm x 7 mm	Tape and Reel	3000	XDPE1A2G7B0000XUMA1 Note 1
				XDPE1A2G7BxyzXUMA1 Note 2

Note:

1. Standard part number with default configuration
2. Customer Specific Configuration File, where x = Firmware ID and yzz = Custom Configuration File ID (Codes assigned by Product Marketing).

Prototype Samples

The customer can program the parts to their specific system requirements using software/hardware available from Infineon or through other controller programming facilities (contact Infineon for recommendations). Infineon Field Application Engineers are available to assist with system and configuration file optimization and programming of the controllers. Alternatively, samples can be ordered with a customer specific custom configuration pre-programmed at the factory, but lead times for these types of samples are significantly longer than for the default configured samples. See **Error! Reference source not found.** for the list of default loop and VOUT configurations. The generic part numbering format is shown below:

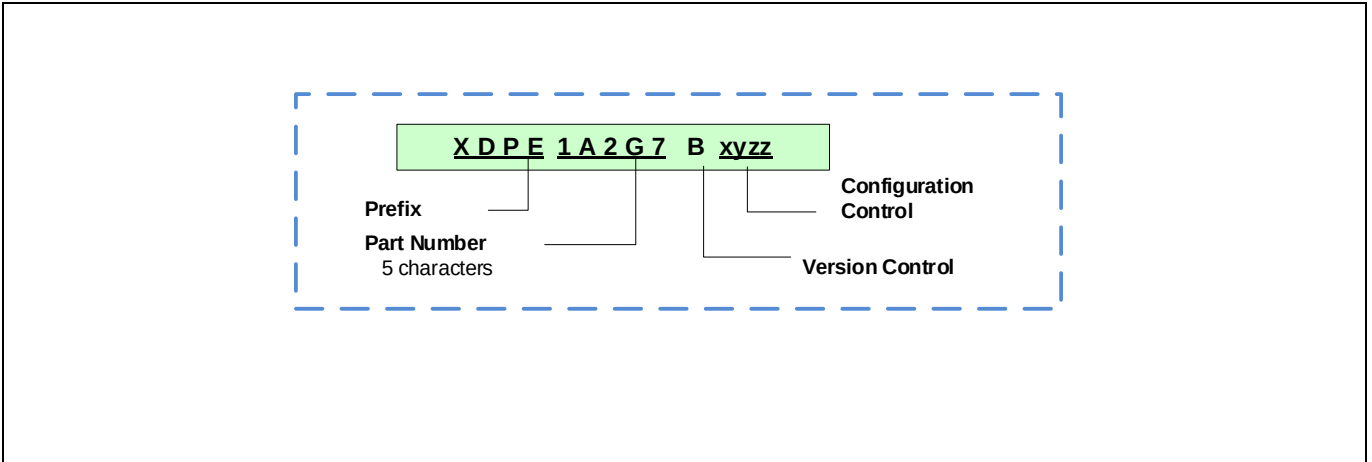


Figure 1 Part number decoding

2 Package marking

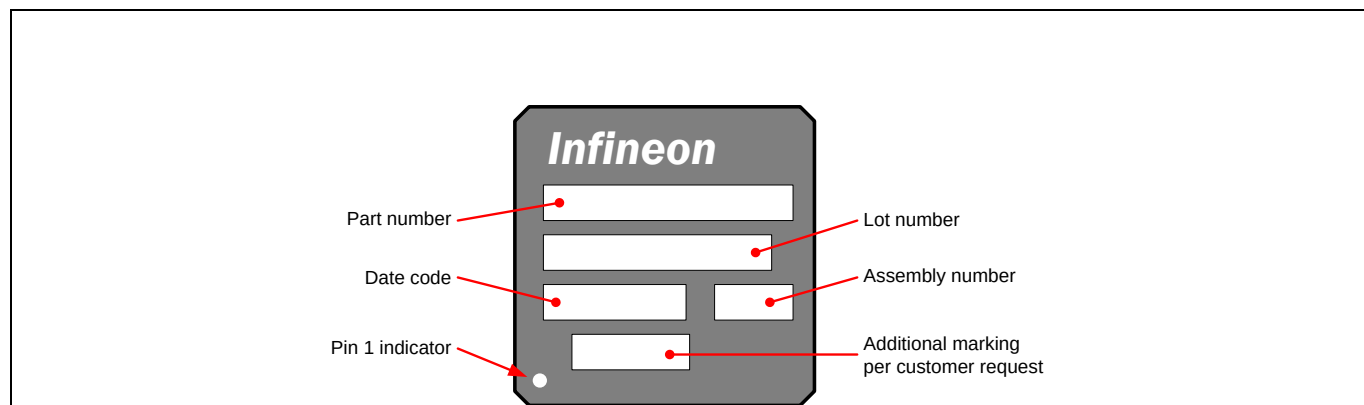


Figure 2 Package marking

3 Environmental qualifications

Table 3

Qualification Level		Industrial	
Moisture Sensitivity		QFN Package	MSL3
ESD	Human Body Model	JS-001, Class 2	
	Charged Device Model	JS-002, Class C3	
	Latch-up	JESD78, Class 2	
RoHS Compliant		Yes	

Revision History

XDPE1A2G7B-short

Revision 2024-04-09, Rev. 2.1

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.0	2024-02-29	Release of final version
2.1	2024-04-09	Remove 'Restricted' from header

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